

**PLEASE AMEND THE SPECIFICATION AS FOLLOWS:**

After the title, insert -- This is a division of Patent Application serial number 10/146,287, filing date 5/15/02, A New Process For Integration Of A High Dielectric Costant Gate Insulator In A Cmos Device, assigned to the same assignee as the present invention.

**REMARKS**

A reference to the parent case has been added by Preliminary Amendment to this Divisional Patent Application.

The application is believed to be in condition for allowance. Allowance of the subject Patent Application is therefore respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to be 'S. Ackerman', with a long horizontal stroke extending to the right.

STEPHEN B. ACKERMAN, REG. NO. 37,761